The XR21V1410 (V1410), XR21V1412 (V1412), and XR21V1414 (V1414) are enhanced 1-channel, 2-channel and 4-channel Universal Asynchronous Receiver and Transmitter (UART) with a USB interface. The USB interface is fully compliant to Full Speed USB 2.0 specification that supports 12 Mbps USB data transfer rate. The USB interface also supports USB suspend, resume and remote wakeup operations.

The V141x operates from an internal 48MHz clock therefore no external crystal/oscillator is required like previous generation UARTs. With the fractional baud rate generator, any baud rate can accurately be generated using the internal 48MHz clock.

The large 128-byte TX FIFO and 384-byte RX FIFO of the V141x helps to optimize the overall data throughput for various applications. The Automatic Transceiver Direction control feature simplifies both the hardware and software for half-duplex RS-485 applications. The multidrop (9-bit) mode with automatic half-duplex transceiver control feature further simplifies typical multidrop RS-485 applications.

The V141x operates from a single 2.97 to 3.63 volt power supply and has 5V tolerant inputs.

Virtual COM port drivers for Windows 2000, XP, Vista, and CE, as well as Linux and Mac are supported for the XR21V1410, XR21V1412 and XR21V1414.
**Ordering Information**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Package</th>
<th>Operating Temperature Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>XR21V1410IL16-F</td>
<td>16-pin QFN</td>
<td>-40°C to 85°C</td>
</tr>
<tr>
<td>XR21V1412IL32-F</td>
<td>32-pin QFN</td>
<td>-40°C to 85°C</td>
</tr>
<tr>
<td>XR21V1414IM48-F</td>
<td>48-pin TQFP</td>
<td>-40°C to 85°C</td>
</tr>
</tbody>
</table>

**Applications**

- Portable Appliances
- External Converters (Dongles)
- Battery-Operated Devices
- Cellular Data Devices
- Factory Automation and Process Controls
- Industrial Applications

**Block Diagram**

- USB Slave Interface
- Internal 48MHz Oscillator
- I²C Interface
- Data+, Data-
- SDA, SCL
- 3.3V VCC, GND
- TXA, RXA
- TXB, RXB
- GPIOA5/RTSA#
- GPIOA4/CTSA#
- GPIOA3/DTRA#
- GPIOA2/DSRA#
- GPIOA1/CDA#
- GPIOA0/RIA#
- UART Channel A
- Fractional BRG
- 128-byte TX FIFO
- 384-byte RX FIFO
- Internal Status and Control Registers
- GPIOs/Modem IO
- UART Channel B (Same as Ch A)
- UART Channel C (Same as Ch A)
- UART Channel D (Same as Ch A)