

# XR88C681 Errata

# **Revision History**

Document No.	Release Date	Change Description
203ER00	July 4, 2022	Initial release.

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### Introduction

This errata indicates a change in the XR88C681 Data Sheet in accordance with PCN 22013.

#### Errata

In the *DC Electrical Characteristics* table, the "For prime grade N, P, J, L, M, ML, VCC = 5V + 10%." third footnote should be changed as follows:

#### Notes

- <sup>1</sup>.Parameters are valid over the specified temperature and operating supply ranges. Typical values are 25°C, V<sub>CC</sub> = 5V and typical processing parameters.
- <sup>2</sup> All voltages are referenced to ground (GND). For testing, input signal levels are 0.4V and 2.4V with a transition time of 20ns maximum. All time measurements are referenced at input voltages of 0.8V and 2.0V as appropriate. See Figure 50.
- $^3$  For prime grade J,  $V_{\rm CC}$  = 5V 10% to + 5%.
- <sup>4</sup> Measured operating with a 3.6864MHz crystal and with all outputs open.



Corporate Headquarters: 5966 La Place Court, Suite 100 Carlsbad, CA 92008 Tel.: +1 (760) 692-0711 Fax: +1 (760) 444-8598

www.maxlinear.com

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