Enhanced +3V or +5V RS-232 Line Driver/Receiver

FEATURES
- Operates from 3.3V or 5V Power Supply
- Meets all EIA-232D and V.28 Specifications at 5V.
- Meets EIA-562 Specifications at 3.3V
- Two Drivers and Receivers
- Operates with 0.1µF to 10µF Capacitors
- High data rate - 120kbps Under Load
- Low Power Shutdown ≤ 1µA
- 3-State TTL/CMOS Receiver Outputs
- Low Power CMOS - 5mA Operation
- Improved ESD Specifications:
  - ±15kV Human Body Model
  - ±15kV IEC61000-4-2 Air Discharge
  - ±8kV IEC61000-4-2 Contact Discharge

DESCRIPTION
Exar's SP385E is an enhanced version of the SP200 family of RS-232 line drivers/receivers. The SP385E offers +3.3V operation for EIA-562 and EIA-232 applications. The SP385E features offered maintains the same performance features offered in its predecessors. The SP385E is available in plastic SOIC or SSOP packages operating over the commercial and industrial temperature ranges. The SP385E is pin compatible to the LTC1385 EIA-562 transceiver with the exception that the drivers are disabled with the ON/OFF pin.

TYPICAL APPLICATION CIRCUIT
ABSOLUTE MAXIMUM RATINGS

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability and cause permanent damage to the device.

- \( V_{CC} = +6.0V \)
- \( V^+ = (V_{CC} - 0.3V) \) to +13.2V
- \( V^- = -13.2V \)

Input Voltages

- \( V_{TXIN} = -0.3V \) to \( V_{CC} + 0.3V \)
- \( V_{RXIN} = +15V \)

Output Voltages

- \( V_{TXOUT} = (V^+, +0.3V) \) to \( V^- - 0.3V \)
- \( V_{RXOUT} = -0.3V \) to \( V_{CC} + 0.3V \)

Short-Circuit Duration

- \( V_{TXOUT} \) continuous

Storage Temperature

- \(-65°C \) to \(+150°C\)

Power Dissipation

- 20-pin SSOP: \( 750mW \) (derate 9.25mW/°C above +70 °C)
- 18-pin Wide SOIC: \( 1260mW \) (derate 15.7mW/°C above +70 °C)

ELECTRICAL CHARACTERISTICS

\( V_{CC} = +3.3V \pm 10\%; \) cap on \( (V^+) \) and \( (V^-) = 1.0\mu F, C1 = C2 = 0.1\mu F; T_{\text{MIN}} \) to \( T_{\text{MAX}} \) unless otherwise noted.

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>TTL INPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Logic Threshold LOW</td>
<td></td>
<td></td>
<td>0.8</td>
<td>V</td>
<td>( V_{TXIN}, ) ON/OFF, ( V_{CC} = 3.3V )</td>
</tr>
<tr>
<td>Input Logic Threshold HIGH</td>
<td></td>
<td></td>
<td>2.0</td>
<td>V</td>
<td>( V_{TXIN}, ) ON/OFF, ( V_{CC} = 3.3V )</td>
</tr>
<tr>
<td>Logic Pullup Current</td>
<td>15</td>
<td></td>
<td>200</td>
<td>( \mu A )</td>
<td>( T_{IN} = 0V )</td>
</tr>
<tr>
<td>Maximum Data Rate</td>
<td>120</td>
<td></td>
<td></td>
<td>kbps</td>
<td>( C_L = 2500pF, R_L = 3k\Omega )</td>
</tr>
<tr>
<td><strong>TTL OUTPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>TTL/CMOS Output Voltage LOW</td>
<td></td>
<td></td>
<td>0.5</td>
<td>V</td>
<td>( I_{OUT} = 3.2mA, V_{CC} = 3.3V )</td>
</tr>
<tr>
<td>TTL/CMOS Output Voltage HIGH</td>
<td></td>
<td></td>
<td>2.4</td>
<td>V</td>
<td>( I_{OUT} = -1.0mA )</td>
</tr>
<tr>
<td>Leakage Current</td>
<td></td>
<td></td>
<td>+0.05</td>
<td>+10</td>
<td>( \mu A )</td>
</tr>
<tr>
<td><strong>EIA-562 OUTPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Voltage Swing</td>
<td>+3.7</td>
<td></td>
<td>+4.2</td>
<td>V</td>
<td>All transmitter outputs loaded with ( 3k\Omega ) to GND</td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS

$V_{cc} = +3.3V \pm 10\%$; cap on $(V+)$ and $(V-) = 1.0\mu F$, $C1 = C2 = 0.1\mu F$; $T_{\text{MIN}}$ to $T_{\text{MAX}}$ unless otherwise noted.

#### EIA-562 OUTPUT (continued)

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Power-Off Output Resistance</td>
<td>300</td>
<td></td>
<td></td>
<td>Ω</td>
<td>$V_{cc} = 0V$, $V_{OUT} = \pm 2V$</td>
</tr>
<tr>
<td>Output Short-Circuit Current</td>
<td>+10</td>
<td></td>
<td></td>
<td>mA</td>
<td>Infinite Duration</td>
</tr>
</tbody>
</table>

#### EIA-562 INPUT

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Voltage Range</td>
<td>-15</td>
<td>15</td>
<td></td>
<td>V</td>
<td></td>
</tr>
<tr>
<td>Input Threshold LOW</td>
<td>0.8</td>
<td>1.2</td>
<td></td>
<td>V</td>
<td>$V_{cc} = 3.3V$, $T_A = +25^\circ C$</td>
</tr>
<tr>
<td>Input Threshold HIGH</td>
<td>1.7</td>
<td>2.4</td>
<td></td>
<td>V</td>
<td>$V_{cc} = 3.3V$, $T_A = +25^\circ C$</td>
</tr>
<tr>
<td>Input Hysteresis</td>
<td>0.2</td>
<td>0.5</td>
<td>1.0</td>
<td>V</td>
<td>$V_{cc} = 3.3V$, $T_A = +25^\circ C$</td>
</tr>
<tr>
<td>Input Resistance</td>
<td>3</td>
<td>5</td>
<td>7</td>
<td>kΩ</td>
<td>$V_{IN} = 15V$ to -15V</td>
</tr>
</tbody>
</table>

#### DYNAMIC CHARACTERISTICS

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Driver Propagation Delay</td>
<td>4.0</td>
<td></td>
<td></td>
<td>µs</td>
<td>TTL to RS-562</td>
</tr>
<tr>
<td>Receiver Propagation Delay</td>
<td>1.5</td>
<td></td>
<td></td>
<td>µs</td>
<td>RS-562 to TTL</td>
</tr>
<tr>
<td>Instantaneous Slew Rate</td>
<td>30</td>
<td></td>
<td></td>
<td>V/µs</td>
<td>$C_L = 10pF$, $R_L = 3k\Omega - 7k\Omega$; $T_A = +25^\circ C$</td>
</tr>
<tr>
<td>Transition-Region Slew Rate</td>
<td>10</td>
<td></td>
<td></td>
<td>V/µs</td>
<td>$C_L = 2500pF$, $R_L = 3k\Omega$; Measured from +2V to -2V or -2V to +2V</td>
</tr>
<tr>
<td>Output Enable Time</td>
<td>300</td>
<td></td>
<td></td>
<td>ns</td>
<td></td>
</tr>
<tr>
<td>Output Disable Time</td>
<td>1000</td>
<td></td>
<td></td>
<td>ns</td>
<td></td>
</tr>
</tbody>
</table>

#### POWER REQUIREMENTS

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Vcc Power Supply Current</td>
<td>3</td>
<td>6</td>
<td></td>
<td>mA</td>
<td>No load, $T_A = +25^\circ C$; $Vcc = 3.3V$</td>
</tr>
<tr>
<td>Vcc Power Supply Current</td>
<td>8</td>
<td></td>
<td></td>
<td>mA</td>
<td>All transmitters $R_L = 3k\Omega$, $T_A = +25^\circ C$</td>
</tr>
<tr>
<td>Shutdown Supply Current</td>
<td>0.01</td>
<td>5</td>
<td></td>
<td>µA</td>
<td>$Vcc = 3.3V$, $T_A = +25^\circ C$</td>
</tr>
</tbody>
</table>
### ELECTRICAL CHARACTERISTICS

\( V_{cc} = +3.3V \pm 10\% \); cap on (V+) and (V-) = 1.0\( \mu \)F; C1 = C2 = 0.1\( \mu \)F; \( T_{min} \) to \( T_{max} \) unless otherwise noted.

#### PARAMETER

<table>
<thead>
<tr>
<th>PARAMETER</th>
<th>MIN.</th>
<th>TYP.</th>
<th>MAX.</th>
<th>UNITS</th>
<th>CONDITIONS</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>TTL INPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Logic Threshold LOW</td>
<td>0.8</td>
<td>V</td>
<td></td>
<td>V</td>
<td>( Vcc ) = +3.3V, ( T_{in} ) = 0V</td>
</tr>
<tr>
<td>Input Logic Threshold HIGH</td>
<td>2.0</td>
<td>V</td>
<td></td>
<td>V</td>
<td>( Vcc ) = +3.3V, ( T_{in} ) = 0V</td>
</tr>
<tr>
<td>Logic Pullup Current</td>
<td>15</td>
<td>200</td>
<td>( \mu A )</td>
<td>( T_{in} ) = 0V</td>
<td></td>
</tr>
<tr>
<td>Maximum Data Rate</td>
<td>120</td>
<td>kbps</td>
<td></td>
<td></td>
<td>( C_L = 2500pF, R_L = 3k\Omega )</td>
</tr>
<tr>
<td><strong>TTL OUTPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>TTL/CMOS Output Voltage LOW</td>
<td>0.4</td>
<td>V</td>
<td></td>
<td>V</td>
<td>( I_{OUT} = 3.2mA, Vcc = 5.0V )</td>
</tr>
<tr>
<td>TTL/CMOS Output Voltage HIGH</td>
<td>3.5</td>
<td>V</td>
<td></td>
<td>V</td>
<td>( I_{OUT} = -1.0mA, Vcc = 5.0V )</td>
</tr>
<tr>
<td>Leakage Current</td>
<td>( \pm 0.05 )</td>
<td>( \pm 10 )</td>
<td>( \mu A )</td>
<td>( Vcc ) = 0V, ( V_{OUT} ) = ( \pm 2V ), ( T_{A} = 25^\circ C )</td>
<td></td>
</tr>
<tr>
<td><strong>EIA-232 OUTPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Voltage Swing</td>
<td>( \pm 5.0 )</td>
<td>( \pm 9 )</td>
<td>V</td>
<td>All transmitter outputs loaded with 3k( \Omega ) to GND</td>
<td></td>
</tr>
<tr>
<td>Power-Off Output Resistance</td>
<td>300</td>
<td>( \Omega )</td>
<td></td>
<td>( Vcc ) = 0V, ( V_{OUT} ) = ( \pm 2V )</td>
<td></td>
</tr>
<tr>
<td>Output Short-Circuit Current</td>
<td>( \pm 18 )</td>
<td>mA</td>
<td></td>
<td>Infinite Duration</td>
<td></td>
</tr>
<tr>
<td><strong>EIA-232 INPUT</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Voltage Range</td>
<td>-15</td>
<td>15</td>
<td>V</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Input Threshold LOW</td>
<td>0.8</td>
<td>1.2</td>
<td>V</td>
<td>( Vcc = 5V, T_{A} = +25^\circ C )</td>
<td></td>
</tr>
<tr>
<td>Input Threshold HIGH</td>
<td>1.7</td>
<td>2.4</td>
<td>V</td>
<td>( Vcc = 5V, T_{A} = +25^\circ C )</td>
<td></td>
</tr>
<tr>
<td>Input Hysteresis</td>
<td>0.2</td>
<td>0.5</td>
<td>1.0</td>
<td>V</td>
<td>( Vcc = 5V, T_{A} = +25^\circ C )</td>
</tr>
<tr>
<td>Input Resistance</td>
<td>3</td>
<td>5</td>
<td>7</td>
<td>k( \Omega )</td>
<td>( V_{IN} = 15V ) to -15V</td>
</tr>
<tr>
<td><strong>DYNAMIC CHARACTERISTICS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Propagation Delay</td>
<td>1.5</td>
<td>( \mu s )</td>
<td></td>
<td>RS-232 to TTL</td>
<td></td>
</tr>
<tr>
<td>Instantaneous Slew Rate</td>
<td>30</td>
<td>V/( \mu s )</td>
<td></td>
<td>( C_L = 10pF, R_L = 3k\Omega - 7k\Omega ), ( T_{A} = +25^\circ C )</td>
<td></td>
</tr>
<tr>
<td>Transition-Region Slew Rate</td>
<td>10</td>
<td>V/( \mu s )</td>
<td></td>
<td>( C_L = 2500pF, R_L = 3k\Omega ); Measured from +3V to -3V or -3V to +3V</td>
<td></td>
</tr>
<tr>
<td>Output Enable Time</td>
<td>400</td>
<td>ns</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Output Disable Time</td>
<td>250</td>
<td>ns</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>POWER REQUIREMENTS</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>( Vcc ) Power Supply Current</td>
<td>10</td>
<td>15</td>
<td>mA</td>
<td>No load, ( T_{A} = +25^\circ C ), ( Vcc = 5V )</td>
<td></td>
</tr>
<tr>
<td>( Vcc ) Power Supply Current</td>
<td>25</td>
<td>mA</td>
<td></td>
<td>All transmitters ( R_L = 3k\Omega ), ( T_{A} = +25^\circ C )</td>
<td></td>
</tr>
<tr>
<td>Shutdown Supply Current</td>
<td>1</td>
<td>10</td>
<td>( \mu A )</td>
<td>( Vcc = 5V, T_{A} = +25^\circ C )</td>
<td></td>
</tr>
</tbody>
</table>
TYPICAL PERFORMANCE CHARACTERISTICS

VIL Voltage (Volts)

VCC = 5V

VCC = 4V

VCC = 3V

Load Current (mA)

Load Current (mA)

VCC (Volts)

VCC (Volts)

Temperature (C)

Temperature (C)

V+ (Volts)

V+ (Volts)

ICC (mA)

ICC (mA)

VCC (Volts)

VCC (Volts)

VOH (Volts)

VOH (Volts)

TA = 25 C

TA = 25 C

PINOUT

ON/OFF

VCC

GND

T1 OUT

R1 IN

R1 OUT

T1 IN

T2 OUT

R2 IN

R2 OUT

N/C

N/C

C1−

C1−

C2−

C2−

C+

C+

V−

V−

T2 OUT

T2 OUT

SP385E

SP385E

18-pin WSOIC

20-pin SSOP

SP385E_101_012920
TYPICAL OPERATING CIRCUIT

+5V INPUT

0.1µF
6.3V

0.1µF
16V

+5V to +10V Voltage Doubler

+10V to -10V Voltage Inverter

TTL/CMOS INPUTS

TTL/CMOS OUTPUTS

R2
10 9 R INR OUT2

R1
13 14 R INR OUT1

T2
11 8 T OUT2

T1
12 15 T OUT1

400kΩ

5kΩ

5kΩ

+0.1µF

6.3V

10µF

1.0µF

16V

5V INPUT

RS232 INPUTS

RS232 OUTPUTS

SP385E

RS232 INPUTS

RS232 OUTPUTS

SP385E

GND

16

GND

18

SOIC Package

SSOP Package

TTL/CMOS

INPUTS

+1.0µF

16V

+0.1µF

+5V to +10V

Voltage Doubler

+10V to -10V

Voltage Inverter

TTL/CMOS

OUTPUTS

RS232

INPUTS

RS232

OUTPUTS
FEATURES
The Exar SP385E is a +3V or +5V EIA-232/EIA-562 line transceiver. It is a pin-for-pin alternative for the SP310A and will operate in the same socket with capacitors ranging from 0.1µF to 10µF, either polarized or non-polarized, in +3V supplies. The SP385E offers the same features such as 120kbps guaranteed transmission rate, improved drive current for longer and more flexible cable configurations, low power dissipation and overall ruggedized construction for commercial and industrial environments. The SP385E also includes a shutdown feature that tri-states the drivers and the receivers.

The SP385E includes a charge pump voltage converter which allows it to operate from a single +3.3V or +5V supply. These converters double the $V_{CC}$ voltage input in order to generate the EIA-232 or EIA-562 output levels. For +5V operation, the SP385E driver outputs adhere to all EIA-232 and CCITT V.28 specifications. While at +3.3V operation, the outputs adhere to EIA-562 specifications. Due to Exar's efficient charge pump design, the charge pump levels and the driver outputs are less noisy than other 3V EIA-232 transceivers.

The SP385E has a single control line which simultaneously shuts down the internal DC/DC converter and puts all transmitter and receiver outputs into a high impedance state.

The SP385E is available in 18-pin plastic SOIC and 20-pin plastic SSOP packages for operation over commercial and industrial temperature ranges. Please consult the factory for surface-mount packaged parts supplied on tape-on-reel as well as parts screened to MIL-M-38510.

The SP385E is ideal for +3.3V battery applications requiring low power operation. The charge pump strength allows the drivers to provide ±4.0V signals, plenty for typical EIA-232 applications since the EIA-232 receivers have input sensitivity levels of less than ±3V.

THEORY OF OPERATION
The SP385E device is made up of three basic circuit blocks — 1) a driver/transmitter, 2) a receiver and 3) a charge pump.

Driver/Transmitter
The drivers are inverting transmitters, which accept TTL or CMOS inputs and output the RS-232 signals with an inverted sense relative to the input logic levels. Typically the RS-232 output voltage swing is ±9V for 5V supply and ±4.2V for 3.3V supply. Even under worst case loading conditions of 3kΩ and 2500pF, the output is guaranteed to be ±5V for a 5V supply and ±3.7V for a 3.3V supply which adheres to EIA-232 and EIA-562 specifications, respectively. The transmitter outputs are protected against infinite short-circuits to ground without degradation in reliability.

The instantaneous slew rate of the transmitter output is internally limited to a maximum of 30V/µs in order to meet the standards [EIA 232-D 2.1.7, Paragraph (5)]. However, the transition region slew rate of these enhanced products is typically 10V/µs. The smooth transition of the loaded output from VOL to VOH clearly meets the monotonicity requirements of the standard [EIA 232-D 2.1.7, Paragraphs (1) & (2)].

Receivers
The receivers convert RS-232 input signals to inverted TTL signals. Since the input is usually from a transmission line, where long cable lengths and system interference can degrade the signal, the inputs have a typical hysteresis margin of 500mV. This ensures that the receiver is virtually immune to noisy transmission lines.

The input thresholds are 0.8V minimum and 2.4V maximum, again well within the ±3V RS-232 requirements. The receiver inputs are also protected against voltages up to ±15V. Should an input be left unconnected, a 5kΩ pull-down resistor to ground will commit the output of the receiver to a high state.

In actual system applications, it is quite possible for signals to be applied to the receiver inputs before power is applied to the receiver circuitry. This occurs for example when a PC user attempts to print only to realize the printer wasn’t turned on. In this case an RS-232 signal from the PC will appear on the receiver input at the printer. When the printer power is turned on, the receiver will operate normally. All of these enhanced devices are fully protected.
Charge Pump
The charge pump is a Exar–patented design (5,306,954) and uses a unique approach compared to older less–efficient designs. The charge pump still requires four external capacitors, but uses a four–phase voltage shifting technique to attain symmetrical 10V power supplies. There is a free–running oscillator that controls the four phases of the voltage shifting. A description of each phase follows.

Phase 1
— V<sub>SS</sub> charge storage —During this phase of the clock cycle, the positive side of capacitors C1 and C2 are initially charged to +5V. C1+ is then switched to ground and the charge in C1– is transferred to C2–. Since C2+ is connected to +5V, the voltage potential across capacitor C2 is now 10V.

Phase 2
— V<sub>SS</sub> transfer — Phase two of the clock connects the negative terminal of C2 to the V<sub>SS</sub> storage capacitor and the positive terminal of C2 to ground, and transfers the generated –10V to C3. Simultaneously, the positive side of capacitor C 1 is switched to +5V and the negative side is connected to ground.

Phase 3
— V<sub>DD</sub> charge storage — The third phase of the clock is identical to the first phase — the charge transferred in C1 produces –5V in the negative terminal of C1, which is applied to the negative side of capacitor C2. Since C2+ is at +5V, the voltage potential across C2 is 10V.

Phase 4
— V<sub>DD</sub> transfer — The fourth phase of the clock connects the negative terminal of C2 to ground, and transfers the generated 10V across C2 to C4, the V<sub>DD</sub> storage capacitor. Again, simultaneously with this, the positive side of capacitor C1 is switched to +5V and the negative side is connected to ground, and the cycle begins again.

Since both V+ and V– are separately generated from V<sub>CC</sub>; in a no–load condition V+ and V– will be symmetrical. Older charge pump approaches that generate V– from V+ will show a decrease in the magnitude of V– compared to V+ due to the inherent inefficiencies in the design.

The clock rate for the charge pump typically operates at 15kHz. The external capacitors can be as low as 0.1µF with a 16V breakdown voltage rating.

Figure 1. Charge Pump Waveform
Shutdown (ON/OFF)

The SP385E has a shut-down/standby mode to conserve power in battery-powered systems. To activate the shutdown mode, which stops the operation of the charge pump, a logic "0" is applied to the appropriate control line. The shutdown mode is controlled on the SP385E by a logic "0" on the ON/OFF control line (pin 18 for the SOIC and pin 20 for the SSOP packages); this puts the transmitter outputs in a tri-state mode.
ESD TOLERANCE

The SP385E device incorporates ruggedized ESD cells on all driver output and receiver input pins. The ESD structure is improved over our previous family for more rugged applications and environments sensitive to electro-static discharges and associated transients. The improved ESD tolerance is at least ±15kV without damage nor latch-up.

There are different methods of ESD testing applied:

   a) MIL-STD-883, Method 3015.7
   b) IEC61000-4-2 Air-Discharge
   c) IEC61000-4-2 Direct Contact

The Human Body Model has been the generally accepted ESD testing method for semi-conductors. This method is also specified in MIL-STD-883, Method 3015.7 for ESD testing. The premise of this ESD test is to simulate the human body’s potential to store electro-static energy and discharge it to an integrated circuit. The simulation is performed by using a test model as shown in Figure 6. This method will test the IC’s capability to withstand an ESD transient during normal handling such as in manufacturing areas where the IC’s tend to be handled frequently.

The IEC-61000-4-2, formerly IEC801-2, is generally used for testing ESD on equipment and systems. For system manufacturers, they must guarantee a certain amount of ESD protection since the system itself is exposed to the outside environment and human presence. The premise with IEC61000-4-2 is that the system is required to withstand an amount of static electricity when ESD is applied to points and surfaces of the equipment that are accessible to personnel during normal usage. The transceiver IC receives most of the ESD current when the ESD source is applied to the connector pins. The test circuit for IEC61000-4-2 is shown on Figure 7. There are two methods within IEC61000-4-2, the Air Discharge method and the Contact Discharge method.

With the Air Discharge Method, an ESD voltage is applied to the equipment under test (EUT) through air. This simulates an electrically charged person ready to connect a cable onto the rear of the system only to find an unpleasant zap just before the person touches the back panel. The high energy potential on the person discharges through an arcing path to the rear panel of the system before he or she even touches the system. This energy, whether discharged directly or through air, is predominantly a function of the discharge current rather than the discharge voltage. Variables with an air discharge such as approach speed of the object carrying the ESD potential to the system and humidity will tend to change the discharge current. For example, the rise time of the discharge current varies with the approach speed.

The Contact Discharge Method applies the ESD current directly to the EUT. This method was devised to reduce the unpredictability of the ESD arc. The discharge current rise time is constant since the energy is directly transferred without the air-gap arc. In situations such as hand held systems, the ESD charge can be directly discharged to the equipment from a person already holding the equipment. The current is transferred on to the keypad or the serial port of the equipment directly and then travels through the PCB and finally to the IC.

Figure 6. ESD Test Circuit for Human Body Model
The circuit models in Figures 6 and 7 represent the typical ESD testing circuit used for all three methods. The $C_S$ is initially charged with the DC power supply when the first switch (SW1) is on. Now that the capacitor is charged, the second switch (SW2) is on while SW1 switches off. The voltage stored in the capacitor is then applied through $R_S$, the current limiting resistor, onto the device under test (DUT). In ESD tests, the SW2 switch is pulsed so that the device under test receives a duration of voltage.

For the Human Body Model, the current limiting resistor ($R_S$) and the source capacitor ($C_S$) are $1.5\,\text{kHz}$ and $100\,\text{pF}$, respectively. For IEC-61000-4-2, the current limiting resistor ($R_S$) and the source capacitor ($C_S$) are $330\,\Omega$ and $150\,\text{pF}$, respectively. The higher $C_S$ value and lower $R_S$ value in the IEC61000-4-2 model are more stringent than the Human Body Model. The larger storage capacitor injects a higher voltage to the test point when SW2 is switched on. The lower current limiting resistor increases the current charge onto the test point.

<table>
<thead>
<tr>
<th>DEVICE PIN TESTED</th>
<th>HUMAN BODY MODEL</th>
<th>IEC61000-4-2</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td>Air Discharge</td>
</tr>
<tr>
<td>Driver Outputs</td>
<td>$\pm 15,\text{kV}$</td>
<td>$\pm 15,\text{kV}$</td>
</tr>
<tr>
<td>Receiver Inputs</td>
<td>$\pm 15,\text{kV}$</td>
<td>$\pm 15,\text{kV}$</td>
</tr>
</tbody>
</table>

Table 1. Transceiver ESD Tolerance Levels
PACKAGE: 18 PIN WSOIC

18 pin WSOIC version is obsolete.
## ORDERING INFORMATION

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Temp. Range</th>
<th>Package</th>
<th>Packaging Method</th>
<th>Lead-Free(2)</th>
</tr>
</thead>
<tbody>
<tr>
<td>SP385ECA-L/TR</td>
<td>0°C to +70°C</td>
<td>20 Pin SSOP</td>
<td>Tape and Reel</td>
<td>Yes</td>
</tr>
<tr>
<td>SP385EEA-L/TR</td>
<td>-40°C to +85°C</td>
<td>20 Pin SSOP</td>
<td>Tape and Reel</td>
<td>Yes</td>
</tr>
</tbody>
</table>

**NOTES:**
1. Refer to [www.maxlinear.com/SP385E](http://www.maxlinear.com/SP385E) for most up to date Ordering Information.
2. Visit [www.maxlinear.com](http://www.maxlinear.com) for additional information on Environmental Rating.
3. 18-pin WSOIC version is obsolete.
## REVISION HISTORY

<table>
<thead>
<tr>
<th>DATE</th>
<th>REVISION</th>
<th>DESCRIPTION</th>
</tr>
</thead>
<tbody>
<tr>
<td>03/08/05</td>
<td>--</td>
<td>Legacy Sipex Datasheet</td>
</tr>
<tr>
<td>03/08/11</td>
<td>1.0.0</td>
<td>Convert to Exar Format, update ordering information and change ESD specification to IEC61000-4-2</td>
</tr>
<tr>
<td>01/29/20</td>
<td>1.0.1</td>
<td>Update to MaxLinear logo. Update ordering information.</td>
</tr>
</tbody>
</table>

MaxLinear, Inc.
5966 La Place Court, Suite 100
Carlsbad, CA 92008
760.692.0711 p.
760.444.8598 f.
www.maxlinear.com

The content of this document is furnished for informational use only, is subject to change without notice, and should not be construed as a commitment by MaxLinear, Inc. MaxLinear, Inc. assumes no responsibility or liability for any errors or inaccuracies that may appear in the informational content contained in this guide. Complying with all applicable copyright laws is the responsibility of the user. Without limiting the rights under copyright, no part of this document may be reproduced into, stored in, or introduced into a retrieval system, or transmitted in any form or by any means (electronic, mechanical, photocopying, recording, or otherwise), or for any purpose, without the express written permission of MaxLinear, Inc.

MaxLinear, Inc. does not recommend the use of any of its products in life support applications where the failure or malfunction of the product can reasonably be expected to cause failure of the life support system or to significantly affect its safety or effectiveness. Products are not authorized for use in such applications unless MaxLinear, Inc. receives, in writing, assurances to its satisfaction that: (a) the risk of injury or damage has been minimized; (b) the user assumes all such risks; (c) potential liability of MaxLinear, Inc. is adequately protected under the circumstances.

MaxLinear, Inc. may have patents, patent applications, trademarks, copyrights, or other intellectual property rights covering subject matter in this document. Except as expressly provided in any written license agreement from MaxLinear, Inc., the furnishing of this document does not give you any license to these patents, trademarks, copyrights, or other intellectual property.

MaxLinear, MxL, Full-Spectrum Capture, FSC, G.now, AirPHY and the MaxLinear logo are all trademarks of MaxLinear, Inc. or one of MaxLinear’s subsidiaries in the U.S.A. and other countries. All rights reserved. Other company trademarks and product names appearing herein are the property of their respective owners.

© 2005 - 2020 MaxLinear, Inc. All rights reserved.